

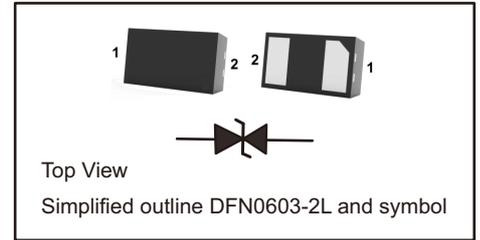


Transient Voltage Suppressors for ESD Protection

General Description

The ESDB3V3DSL2A is designed to protect voltage sensitive components from ESD and transient voltage events.

Excellent clamping capability, low leakage, and fast response time, make these parts ideal for ESD protection on designs where board space is at a premium.



Features

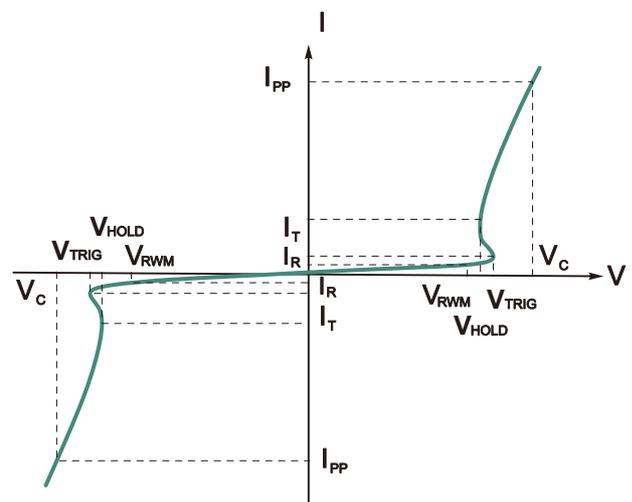
- 110 Watts peak pulse power ($t_p = 8/20\mu s$)
- Uni-directional ESD protection of one line
- IEC 61000-4-2 (ESD) $\pm 30kV$ (air), $\pm 30kV$ (contact)
- Suitable replacement for MLV's in ESD protection applications
- Working voltages: 3.3V
- Low leakage current
- Low clamping voltage

APPLICATIONS

- Computers and peripherals
- Audio and video equipment
- Cellular handsets and accessories
- Portable electronics
- Other electronics equipments communication systems

Electronics Parameter

Parameter	Symbol
Maximum Reverse Peak Pulse Current	I_{PP}
Clamping Voltage @ I_{PP}	V_C
Peak Reverse Working Voltage	V_{RWM}
Reverse Leakage Current @ V_{RWM}	I_R
Breakdown Viltage @ I_T	V_{BR}
Test Current	I_T
Forward Current	I_F
Forward Voltage @ I_F	V_F





Absolute Ratings
(Tamb=25°C)

Parameter	Symbol	Value	Unit
IEC 61000-4-2 ESD Voltage Air Model	V _{ESD}	±30	KV
IEC 61000-4-2 ESD Voltage Contact Model		±30	
Peak Pulse Power(tp=8/20us)	P _{pk}	110	W
Peak Pulse Current(tp=8/20us)	I _{PP}	10	A
Maximum junction temperature	T _J	-55 to +125	°C
Storage Temperature	T _{STG}	-55 to +150	°C

Electrical Characteristics

Ratings at 25 °C ambient temperature unless otherwise specified.

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Reverse Stand-off voltage	V _{RWM}				3.3	V
Reverse Breakdown Voltage	V _{BR}	I _T =1mA	3.5	4.2		V
Reverse Leakage Current	I _R	V _R =V _{RWM}			0.2	uA
Clamping Voltage	V _C	I _{PP} =1A, tp=8/20us			6	V
Clamping Voltage	V _C	I _{PP} =10A, tp=8/20us			11	V
Junction Capacitance	C _j	V _R =0V, f=1MHz		15	20	pF



Fig.1 8 X 20us Pulse Waveform

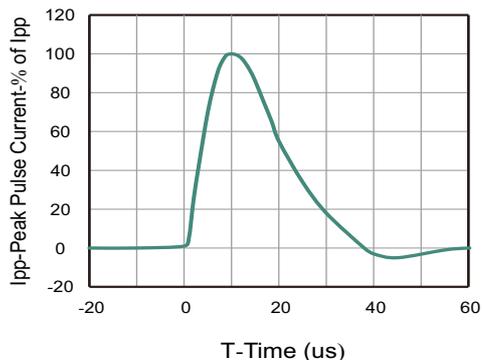


Fig.2 Power Derating Curve

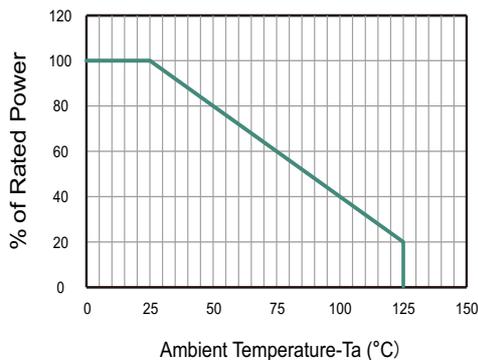


Fig.3 Clamping Voltage vs. Peak Pulse Current

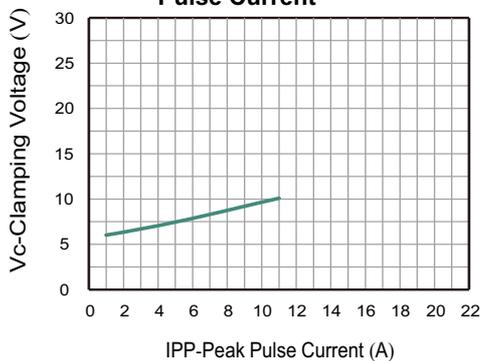


Fig.4 Junction Capacitance vs. Reverse Voltage

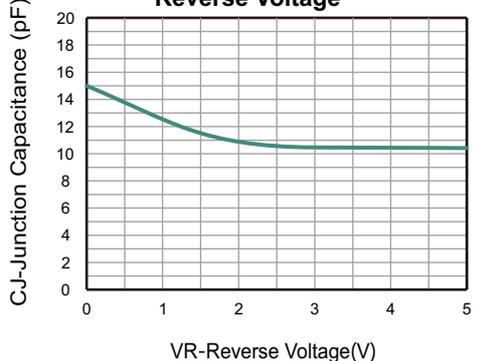


Fig.5 Peak Pulse Power vs. Pulse Time

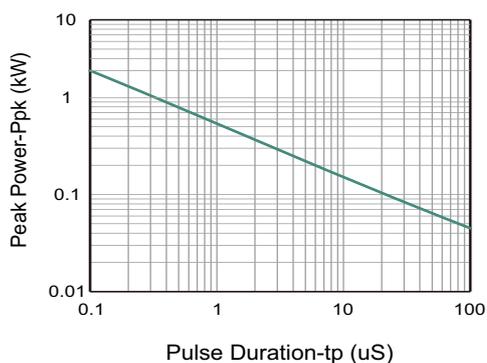
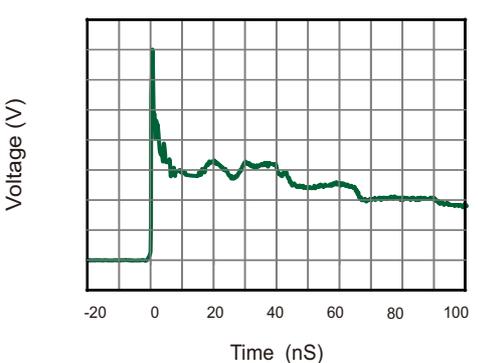
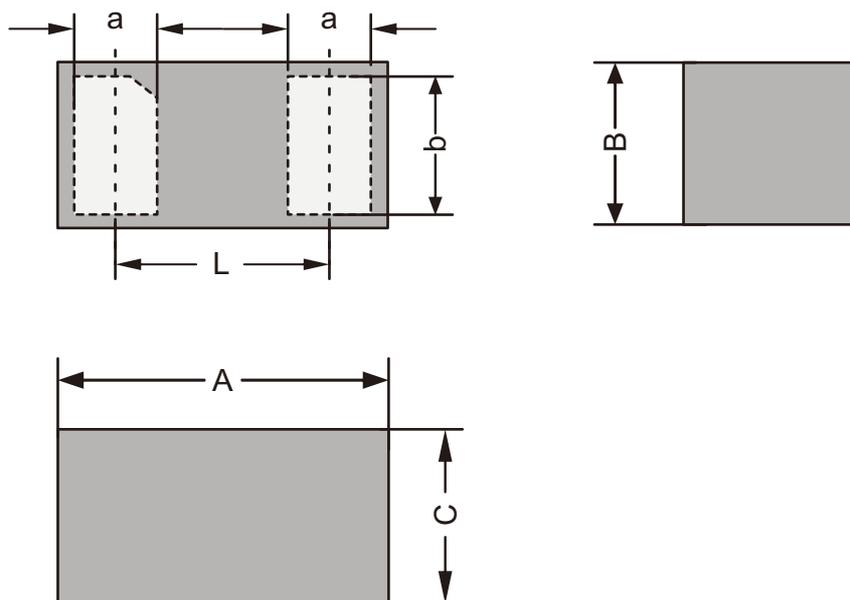


Fig.6 IEC61000-4-2 Pulse Waveform





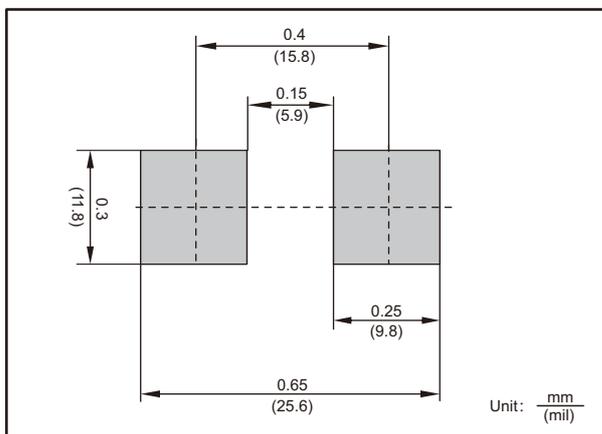
DFN0603-2L Package Outline Dimensions



DFN0603-2L mechanical data

UNIT		A	B	C	a	b	L
mm	max	0.65	0.35	0.34	0.19	0.26	0.36
	min	0.58	0.28	0.28	0.13	0.20	
mil	max	25.59	13.78	13.39	7.48	10.24	14.17
	min	22.83	11.02	11.02	5.12	7.87	

The recommended mounting pad size



Marking

Type number	Marking code
ESDB3V3DSL2A	d



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